

Application No. : 09/808,469
Filed : March 14, 2001

IN THE SPECIFICATION

1. On page 20, lines 14-20 of the specification, please amend the text as follows:

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-- Referring now to Fig. 33, the method 3300 of synthesizing a processor design incorporating the compressed instruction set previously discussed is described. The generalized method of synthesizing integrated circuits having a user-customized (i.e., "soft") instruction set is disclosed in Applicant's co-pending U.S. Patent Application Serial No. 09/418,663 entitled "Method And Apparatus For Managing The Configuration And Functionality Of A Semiconductor Design" filed October 14, 1999, now U.S. Patent No. 6,862,563, which is incorporated herein by reference in its entirety. --